

António Ribeiro

CONTACT INFO

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- franciscorente.dev

EXPERIENCE

Master's research

SpecsLab, INESC-TEC

FEUP, Porto

2023–2024

- CGRA-RISC
- Worked on a co-simulation infrastructure that couples a Coarse-Grained Reconfigurable Array (CGRA) simulator, built in Java, to a RISC-V simulator, X-HEEP. The work also proposes a RISC-V extension, with instructions that configure and command the accelerator.

Research Intern

SpecsLab, INESC-TEC

FEUP, Porto

2022

- Optimizing a decision tree for a FPGA
- Worked on improving the performance of HLS code (C++) for an Hoeffding tree model running on an FPGA board.

EDUCATION

Faculdade Engenharia da Universidade do Porto

Masters in Informatics and Computing Engineering

Grade: 18/20

2022–2024

Faculdade Engenharia da Universidade do Porto

Bachelor in Informatics and Computing Engineering

Grade: 17/20

2019–2022

PROJECTS

See full list of projects on franciscorente.dev/projects

- ArUcoBot (C++, 2023)
 - Real time system of an Alphabot that completes ArUco marker instructions
- FEUP-BDNR (React/Express, 2023)
 - Store management system, built on top of real-life reviews and Couchbase NoSQL DB

SKILLS

- **Languages:** C/C++, JavaScript, Python, Java, SQL, PHP, Bash Script, and HTML/CSS
- **Frameworks and tools:** Git, React, Make, Docker, JUnit, Numpy, and Pandas
- **Language:** English proficiency, reading and speaking

INTERESTS

- Maths and Physics
- Electronics and Embedded Systems
- Systems' Design